

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property
Organization
International Bureau



(43) International Publication Date
29 September 2005 (29.09.2005)

PCT

(10) International Publication Number
WO 2005/091303 A1

(51) International Patent Classification⁷: **G11C 16/10,**
G06F 9/445

West Forest Gate, Wellington Road, Wokingham, Berkshire RG40 2AQ (GB).

(21) International Application Number:
PCT/GB2005/000788

(74) Agent: **GILL JENNINGS & EVERY**; Broadgate House,
7 Eldon Street, London EC2M 7LM (GB).

(22) International Filing Date: 2 March 2005 (02.03.2005)

(81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NA, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SM, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:
0406237.8 19 March 2004 (19.03.2004) GB

(71) Applicant (for all designated States except US): **MAT-SUSHITA ELECTRIC INDUSTRIAL CO., LTD.** [JP/JP]; Matsushita IMP Building 19th Floor, 1-3-7 Shiromi Chuo-ku, Osaka 540-6319 (JP).

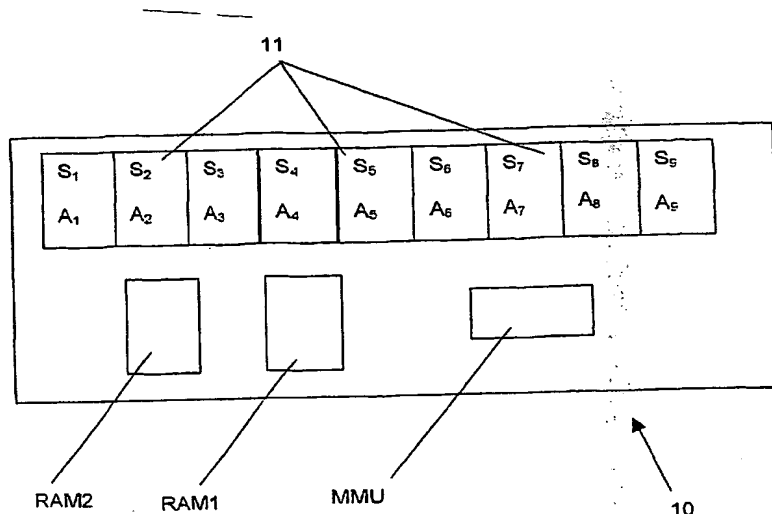
(84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IS, IT, LT, LU, MC, NL, PL, PT, RO,

(72) Inventor; and

(75) Inventor/Applicant (for US only): **GORDON, David,** George [GB/GB]; Panasonic Mobile Communications,

[Continued on next page]

(54) Title: **REPROGRAMMING A NON-VOLATILE SOLID STATE MEMORY SYSTEM**



(57) Abstract: A non-volatile memory system (10) is provided. The system comprises: non-volatile memory (11) divided into a plurality of segments (11) each segment having an address in an address space, means for copying any one segment to be reprogrammed into a first RAM (RAM1), the first RAM having a size at least equal to the segment size. The system further comprises a second RAM (RAM2) for holding a reprogrammed code, writing means for writing the reprogrammed code from the second RAM into the at least one segment to be reprogrammed, and control means (MMU) arranged to enable execution of the programme from the first RAM during the reprogramming.

WO 2005/091303 A1



SE, SI, SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

Published:

- *with international search report*
- *before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments*